AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.   (Currently Amended) A circuit design support method for instructing a computer to execute a program describing an iterative calculation equation of ~~the~~ a Newton method including ~~the~~ a Jacobi matrix to thereby calculate a circuit element value of an analog electronic circuit to be designed, wherein the program, that described ~~describing~~ the iterative calculation equation in which an approximate equation is substituted for a partial differentiation which is ~~that is~~ an element of the Jacobi matrix is used, and the element ~~as an object of the partial differentiation~~ of the Jacobi matrix is obtained from a waveform observed in response to a circuit configuration of the analog electronic circuit.

2.   (Currently Amended) The circuit design support method according to claim 1, wherein a steady state of the analog electronic circuit is obtained simultaneously with ~~the~~ a calculation of the circuit element value.

3.   (Currently Amended) The circuit design support method according to claim 1 , wherein statistical data ~~are~~ is provided as a constraint condition of the analog electronic circuit.

4.   (Currently Amended) A circuit design support implement for executing a program describing an iterative calculation equation of ~~the~~ a Newton method including ~~the~~ a Jacobi matrix to thereby calculate a circuit element value of an analog electronic circuit to be designed, wherein the program, that describes ~~describing~~ the iterative calculation equation in which an approximate equation is substituted for a partial differentiation which is ~~that is~~ an

element of the Jacobi matrix, is used, and the element ~~as an object of the partial differentiation~~ of the Jacobi matrix is obtained from a waveform observed in response to a circuit configuration of the analog electronic circuit.

5.   (Currently Amended) The circuit design support implement according to claim 4, wherein a steady state of the analog electronic circuit is obtained simultaneously with ~~the~~ a calculation of the circuit element value.

6.   (Currently Amended) The circuit design support implement according to claim 4, wherein statistical data ~~are~~ is provided as a constraint condition of the analog electronic circuit.

7.   (Currently Amended) A circuit design support program for instructing a computer to calculate an iterative calculation equation of ~~the~~ a Newton method including ~~the~~ a Jacobi matrix to thereby calculate a circuit element value of an analog electronic circuit to be designed, wherein the iterative calculation equation ~~in which~~ uses an approximate equation ~~is substituted~~ to substitute for a partial differentiation ~~that is~~ which is an element of the Jacobi matrix ~~is used~~, and the element ~~as an object of the partial differentiation~~ of the Jacobi matrix is obtained from a waveform observed in response to a circuit configuration of the analog electronic circuit.

8.   (Currently Amended) The circuit design support program according to claim 7, wherein a steady state of the analog electronic circuit is obtained simultaneously with ~~the~~ a calculation of the circuit element value.

9.   (Currently Amended) The circuit design support program according to claim 7, wherein statistical data ~~are~~ is provided as a constraint condition of the analog electronic circuit.